

CLAIM AMENDMENTS

1. (Currently Amended) In a multi-reference switching amplifier of the type wherein switching devices control power supplied to a load in response to an input data stream having a sign, a simplified output topology, comprising:

a plurality of effective references; [[and]]

~~circuitry for summing the references at the load through linear and switched control of the switching devices.~~

a data separator operative to separate the input data stream into course- and fine-resolution data;

circuitry for modulating a first one of the effective references on one side of the load as a function of the coarse-resolution data; and

circuitry for modulating a second one of the effective references on the other side of the load as a function of the fine-resolution data.

2. (Original) The simplified output topology of claim 1, wherein the effective references are independently-modulated.

3. (Canceled)

4. (Currently Amended) The simplified output topology of claim [[3]] 1, wherein the first one of the effective references is the supply rail.

5. (Currently Amended) The simplified output topology of claim [[3]] 1, wherein the second one of the effective references is derived through a voltage divider.